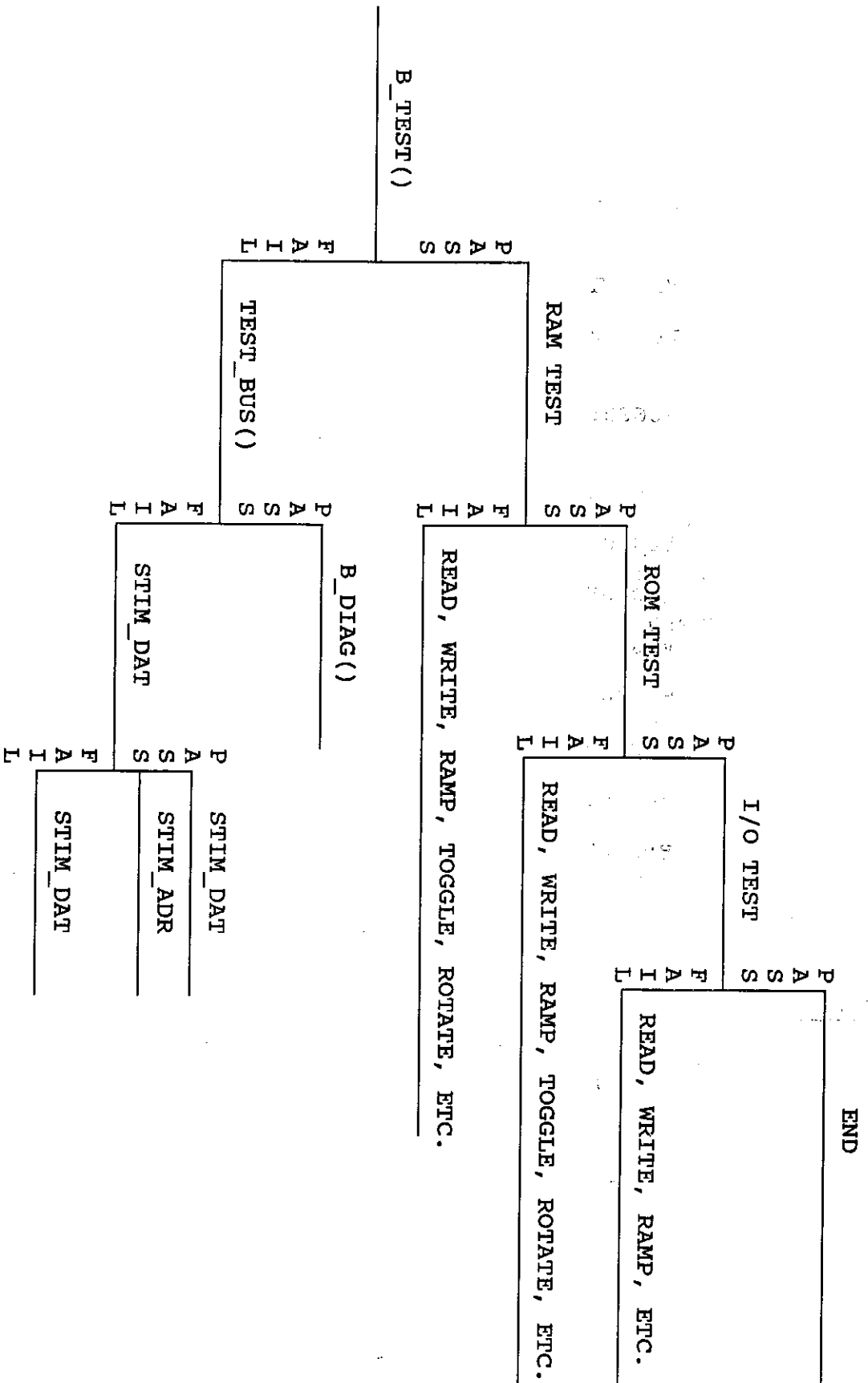
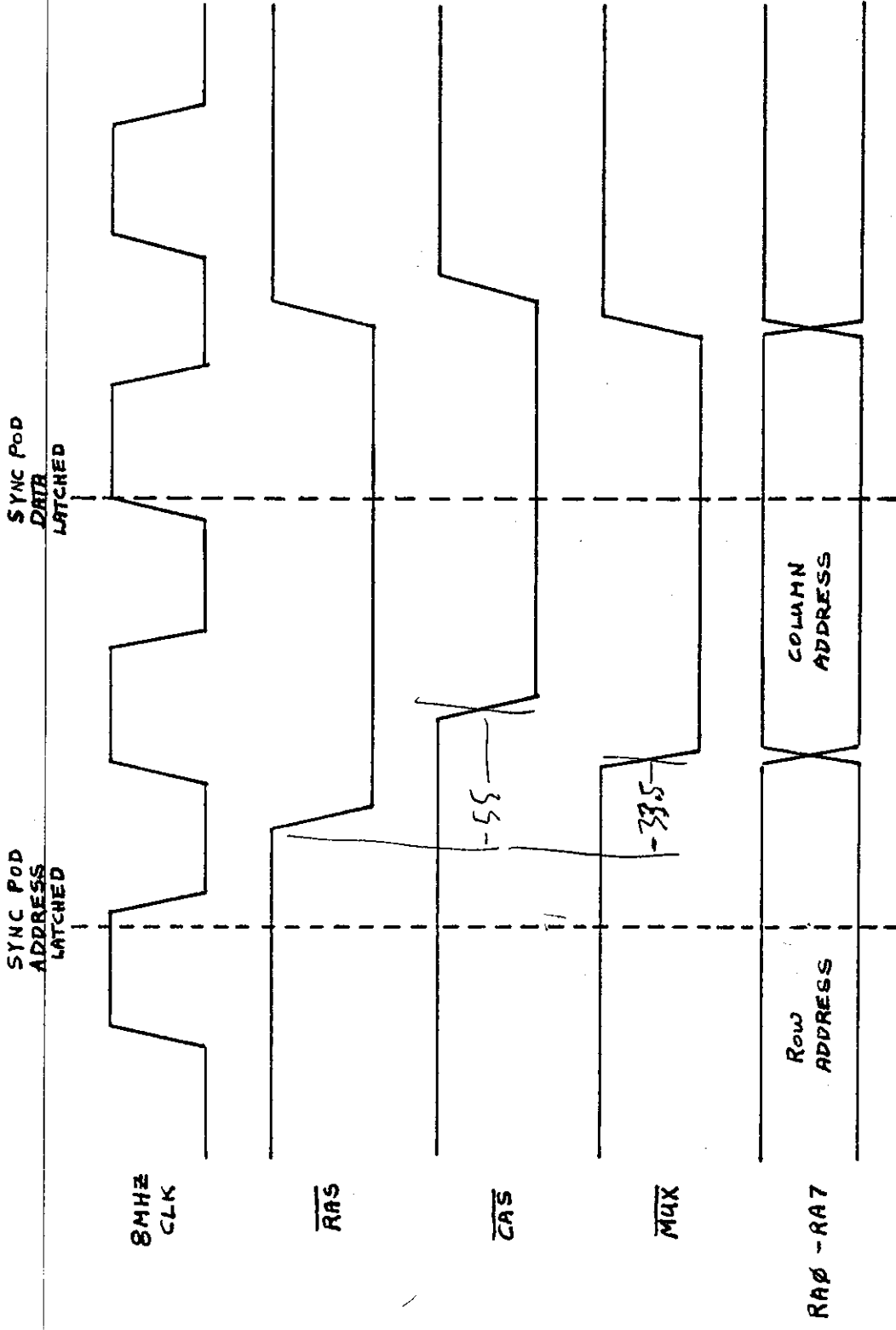


9100A FUNCTIONAL TEST AND STIMULUS FLOW FOR MDE





DYNAMIC RAM ADDRESS TIMING

(no change)

TECHNICAL DATA SHEETS

TITLE	NO.
9100A Series Digital Testers	A0266C
9132A Memory Interface Pod	A0321A
Microprocessor Pod Adapters	A0355B
Vector Output I/O Module	A0357A
9100A Synchronizing I/O Module and Probe with "Int" Mode	A0367A
9132A-68030 Processor Support Pkg.	A0372A
9132A-64180 Processor Support Pkg.	A0373A
9132A-68020 Processor Support Pkg.	A0374A
9132A-80386 Processor Support Pkg.	A0379A
9132A-80386SX Processor Support Pkg.	A0380A
9132A-68000 Processor Support Pkg.	A0384A
9132A-80486 Processor Support Pkg.	A0385A
9132A-7810 Processor Support Pkg.	A0386A
9132A-80286 Processor Support Pkg.	A0387A
9100A-030 9010 to 9100A Programming Translator	A0392A
IEEE 488/Plus Option 9105A-015/ 9100A-015 UGK	A0393B
9132A-68HC11 Processor Support Pkg.	A0406A
Using Multiple I/O Modules For Driving Patterns and Gathering Signatures	L0045A
Putting a Software Switch Into Your GFI Database	L0046A

END

FOR SOFT COPY OF THIS DOCUMENT CONTACT 9100A

**APPLICATION NOTES**

TITLE	NO.
Determining the Suitability of Digital Designs For Testing With The 9132A Memory Interface Pod	B0186A
9100A Testing of a CGA Card With The Vector I/O Module	B0192A
Developing Test Vectors For a Video Controller Chip	B0200A
Testing DMA Circuitry on the IBMPC-AT	A0350A
Adding IEEE-488 Instruments to The 9100A Digital Test System	A0349A
9100A Production Testing	L0030A